

Profile

Name of the Faculty	Sri Lakshmi.Chandana					
Designation	Assistant Professor					
Department	ECE					
Date of Birth	06.06.1986					
DOJ the Institution	04.10.2008					
Employee ID	ECE010069					
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AICTE Unique ID	1-2493301864			AAPAR ID: '965831173956		
Education Qualifications	UG	PG	Ph.D	Others		
	B.Tech.	M.Tech				
Work experience	Teaching	Research		Industry	Others	
	15 years	--		---		
Area of Specialization	Embedded systems					
<p>Courses taught at Under Graduate/ Post Graduate Level: (full course title only)</p> <ul style="list-style-type: none"> Electronic Devices & Circuits Microprocessors and Interfacing Embedded and Real Time Systems Embedded systems Electronic Devices and circuits Basic Electronics Engineering Microcontrollers and Applications Real Time Operating Systems Microprocessors and Microcontrollers Signals and Systems IEC Computer Architecture and Organization VLSI Internet Of Things SENSORS AND REMOTE APPLICATIONS EMI/EMC MEFA COMPUTER ARCHITECTURE AND ORGANIZATION 						
Professional Memberships & ID's : ISTE,ISSE						
Research Publications & Guidance	National Journals	International Journals	National Conferences	International Conferences	Patents	Books / Book chapters
	-	5	---	1	---	---
	Masters			-		

<p>Details of Paper Publications (In IEEE Format only Descending order)</p>	<p>International Journal:</p> <ol style="list-style-type: none"> 1. Ch.Sri Lakshmi "Smart Agriculture: IoT and Machine Learning for Crop Monitoring and Precision Farming"International Journal of INTELLIGENT SYSTEMS AND APPLICATIONS IN ENGINEERING,ISSN 2147-6799,VOL 12(21S),PG.NO:266-273,MARCH 2024 2. Ch.Sri Lakshmi "Intelligent Mobile Edge Computing Integrated with Blockchain Security Analysis for Millimetre-Wave Communication" International Journal of Communication Networks and Information Security,ISSN 2076-0930,VOL 14,ISSUE 3,PG.NO:110-122,DEC 2022 3. Ch.Sri Lakshmi "Selective mapping technique with Novel Symmetric Hankel Phase Sequences for PAPR REDUCTION IN OFDM SYSTEMS" International journal of mechanical engineering,ISSN 0974-5823,VOL 7,ISSUE 5,PG.NO:10-47,MAY 2022 4. Ch.Sri Lakshmi," Secure Data Communication using Cryptography International Journal of Engineering and Advanced Technology Published in Volume 9 Issue 4, April -2020. 5. Ch.Sri Lakshmi, "A SIMPLE APPLICATION OF FM0 ENCODING IN DSRC APPLICATIONS", International Journal of Emerging Technologies and Innovative Research (www.JETIR.org) ISSN UGC Approved & 5.87 Impact Factor Published in Volume 5 Issue 10 , October-2018 . 6. Ch.SriLakshmi," WIRELESS Switch Using Cd4027 AND J-K Flip Flop" International Journal of Emerging Technologies and Innovative Research (www.JETIR.org) ISSN UGC Approved & 5.87 Impact Factor Published in Volume 5 Issue 10 , October-2018. 7. Ch.Sri Lakshmi, "Improved Response of Genetic Simulated Annealing Algorithm for PAPR Reduction in OFDM System using PTS Technique" Indian Journal of ScienceandTechnology, Vol 9(S1), DOI: 10.17485/ijst/2016/v9iS1/107826, December 2016 ISSN (Print) : 0974-6846 ISSN (Online) : 0974-5645. 8. Ch.Sri Lakshmi Sri Lakshmi. "A Noval method to improve the Logic Test by using Single Cycle Access Structure" I Int. Journal of Engineering Research and Applications www.ijera.com ISSN : 2248-9622, Vol. 3, Issue 6, Nov-Dec 2013, pp.1721-1726. <p>International Conference:</p> <ol style="list-style-type: none"> 1. Sri Lakshmi.Chandana "An intelligent sensing and detection system for accident preventions in four wheeler vehicles" Materials Today: Proceedings Vol. 80, Issue 3, Jan 2023, pp. 1713-1716.
<p>Details of Patents Published / Granted</p>	<p>----</p>

<p>Details of Certification Courses / Workshops / STTP's / FDP (participated / organized)</p>	<p><u>FDPS:</u></p> <ol style="list-style-type: none"> 1. Participated & completed successfully AICTE Training And Learning (ATAL) Academy Online Elementary FDP on " Next Gen Communications for IoT and Industrial Automation" from 20/01/2025 to 25/10/2025 at HOLY MARY INSTITUTE OF TECHNOLOGY & SCIENCE. 2. Participated & completed successfully AICTE Training And Learning (ATAL) Academy Online Elementary FDP on " Artificial Intelligence Based Energy Storage Systems Integrated With Renewable And Sustainable Energy Sources For Electric Vehicles At Nri Institute Of Technology" From 2/012/2024 To 14/2/2024 At Nri Engineering College. 3. Five day Faculty Development Program on NBA Accreditation conducted by NITTTR at PVPSIT during 20-24th feb, 2023.. 4. Five day Faculty Development Program on Antenna & Wireless Technologies conducted by NITTTR at PVPSIT during 16-19th September, 2024.. 5. AICTE sponsored One Week Online Short Term Training Programme (STTP) On "MACHINE LEARNING (With Python)" Under AICTE Quality Improvement Scheme (AQIS) Organized by Department of Computer Science & Engineering, Information Technology and Electronics & Telecommunication Engineering Dr. J. J. Magdum College of Engineering, Jaysingpur, Maharashtra from 08th to 12th February, 2022. 6. Participated & completed successfully AICTE Training And Learning (ATAL) Academy Online Elementary FDP on "Internet of Things (IoT): Architecture & System Level Design" from 27/09/2021 to 01/10/2021 at USIC&T, Guru Gobind Singh Indraprastha University. 7. participated in Faculty Development Programme on Internet of Things (IoT) Architecture & System Level Design organized by USIC&T, Guru Gobind Singh Indraprastha University during 27th September – 1st October' 2021. 8. Completed Two Weeks Short Term Training Programme through ICT Mode on Outcome Based Curriculum – Design and Implementation organised by NATIONAL INSTITUTE OF TECHNICAL TEACHERS INSTITUTION KOLKATA from 2nd August to 13th August, 2021 successfully. 9. Successfully completed the course Avr microcontroller programming From 12.07.2021 to 17.7.2021. 10. Participated in Three Day Faculty Development Program (Online) on Research Trends in Deep Learning and Communication Networks, organized by Department of Electronics & Communication Engineering, Bapatla Engineering College, Andhra Pradesh from 28 th to 30th June, 2021 11. Five day Faculty Development Program on NBA Accreditation conducted by NITTTR at PVPSIT during 22nd -26th April, 2019. 12. one week ICT based Faculty Development Program on VLSI Design conducted by NITTTR Chandigarh, organized by dept. of ECE during 24th – 28th September. 13. one week FDP on "CMOS Integrated Circuits for Instrumentation
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	<p>and IOT Applications” conducted by VRSEC in association with NIT Warangal from 22nd to 28th January, 2018.</p> <p>14. . One week Faculty Development Programme was conducted on “New Trends and Methodologies in Engineering Education” in association with E&ICT Academy-NIT Warangal from 24th April, 2017 to 29th April, 2017.</p> <p>15. . A Two day Faculty Development Program was conducted on “Mentor Graphics EDA Tools” on 17th April, 2017 and 18th April, 2017</p> <p><u>WORKSHOPS:</u></p> <ol style="list-style-type: none"> 1. 3-day National Workshop on Recent Trends in Radar Signal Processing organised by the Department of Electronics and Communication Engineering and technically sponsored by I E T E, New Delhi from 3rd – 5th January, 2022. 2. workshop on IOT organized by APSSDC & Pantech e Learning during 18th September - 18th October’ 2021. 3. participated in a workshop on Application Specific Integrated Circuit Design (ASIC) Using Mentor Graphics Tools organized by Department of ECE, PVPSIT during 14th – 15th December’ 2021. 4. One day workshop on VLSI Design and Challenges on 17th November, 2018 sponsored by AICTE under Magadarshan scheme & organized by the ECE, VRSEC. 5. attended Three day workshop on “Scientific Engineering Educational Practices“ at VRSEC, IQAC from 27th January, 2017 to 29th January, 2017. 6. A Three day training program was conducted on “Image Processing with Compact RIO” from 23rd February, 2017 to 25th February, 2017. 7. .A One week on-line workshop was conducted on “ISTE STTP CMOS mixed signal and Radio Frequency” from 30th January, 2017 to 4th February, 2017 organised by IIT, Kharagpur at PVPSIT. <p><u>SEMINARS:</u></p> <ol style="list-style-type: none"> 1. Attended Guest Lecture on Applications of Signals & Systems. 2. Attended Guest Lecture on Introduction To Smart Cities on December 17,2021. 3. Participated in a webinar on New Age Digital Learning Techniques conducted by P N Panicker Foundation, on 3rd July’2021. 4. Attended seminar on IPR7 Patents on 29th September, 2018 conducted by ISTE faculty chapter in association with IQAC.
Details of Projects Applied / Awarded	----
Any Other information	