


Profile

Name of the Faculty	PRAVEENA BAI DESAVATHU					
Designation	ASSISTANT PROFESSOR					
Department	ECE					
Date of Birth	05.04.1984					
DOJ the Institution	08.06.2011					
Employee ID	ECE 060477					
Address for communication	Satya Residency Flat No:513 Opp.Kamineni Hospital Kanuru,Vijayawada-7	Permanent Address		Satya Residency Flat No:513 Opp.Kamineni Hospital Kanuru,Vijayawada-7		
Email-ID	Praveena.d@pvpsiddhartha.ac.in					
AICTE Unique ID	1-454503333			AAPAR ID: 277545682050		
Education Qualifications	UG	PG	Ph.D	Others		
	B.Tech.	M.Tech	Pursuing			
Work experience	Teaching	Research		Industry	Others	
	16			--	--	
Area of Specialization	VLSI SYSTEM DESIGN					
Courses taught at Under Graduate/ Post Graduate Level: (full course title only) VLSI DESIGN, SWITCHING THEORY AND LOGIC DESIGN, DIGITAL INTEGRATED CIRCUITS AND APPLICATIONS, SIGNALS AND SYSTEMS, CELLULAR AND MOBILE COMMUNICATIONS, MICRO PROCESSOR AND MICRO CONTROLLER, ELECTRONIC DEVEICES AND CIRCUITS, ELECTRONIC INSTRUMENTATION, ANALOG ELETRONICS AND CIRCUITS, BASIC ELETRONICS ENGINEERING						
Professional Memberships & ID's : IETE, ISTE						
Research Publications & Guidance	National Journals	International Journals	National Conferences	International Conferences	Patents	Books / Book chapters
	-					
	Masters	7	01	01	--	---

<p>Details of Paper Publications (In IEEE Format only Descending order)</p>	<p>Journals:</p> <ol style="list-style-type: none"> 1. Mrs. D.Praveena Bai published a paper entitled Efficient Way to Measure the Oxyzen in Water Using IOT Devices- Journal of Theoretical and Applied Information Technology(JATIT),ISSN: 1992-8645, Vol:108; Issue:08; Page No: 3523 -3527, April– 2024. (Scopus) 2. Mrs. D.Praveena Bai published a paper entitled Design of low voltage high output resistance current mirror - International Journal of Experimental Research and Review(IJERR), ISSN: 2455-4855; Vol:30; Issue:xx; Page No: 534-539, April– 2023. (Scopus) 3. Mrs. D.Praveena Bai published a paper entitled Implementation of Data Transmission from PC To PC Using Lifi Technology- Asian Journal of Organic & Medicinal Chemistry, ISSN: 2456-8937, Vol:7; Issue:2; Page No: 318-324, June– 2022. (UGC) 4. Mrs. D.Praveena Bai published a paper entitled Certain Investigation on the Methods used for the Detection and Prediction of Rainfall in Andhra Pradesh- Turkish Journal of Physiotherapy and Rehabilitation ISSN: 2651-4451, Vol:32; Issue:3; Page No: 6465-6473, June– 2021. 5. Mrs. D.Praveena Bai published a paper entitled Portrayal Matching Algorithm By using Sift - International Journal of Recent Technology and Engineering (IJRTE), ISSN: 2277-3878, Vol:7; Issue:2; Page No: 2711-2713, May– 2020. 6. Mrs. D.Praveena Bai published a paper entitled WIRELESS SWITCH USING CD4027AND J-K FLIPFLOP- Journal of Emerging Technologies and Innovative Research (JETIR), ISSN: 2349-5162, Vol:5; Issue:10; Page No: 323-330, OCT– 2018. 7. Mrs. Ch. Sri Lakshmi, Assistant Professor & Mrs. D. Praveena Bai, Assistant Professor published a paper entitled A Simple Application of FM 0 Encoding in DSRC Applications - JETIR,(UGC Approved) ISSN:2349-5162; Vol.5, Issue 10, Impact factor : 5.87; October - 2018. <p>Conferences:</p> <p>International Conference</p>
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	<ol style="list-style-type: none"> 1. Mrs. D.Praveena Bai published a paper entitled “Design and Implementation of CNN-FPGA accelerator based on Open Computing Language” International Conference on Electrical, Electronics, Information and Communication Technologies (ICEEICT 2022),vol: 1,Issue: 1, 16- 18 February 2022(Scopus) <p>National Conference:</p> <ol style="list-style-type: none"> 1. A Simple Application of FM0 Encoding in DSRC Applications, Volume 2, Special Issue 01,October 2016, ISSN: 2455-3778
Details of Patents Published / Granted	--
Details of Certification Courses / Workshops / STTP's / FDP (participated / organized)	<ol style="list-style-type: none"> 1. D.Praveena Bai attended Faculty Development Programme on NEP Orientation and Sensitization Programme Organized by MMTTC in association with Assam University during 5th – 14th Feb, 2025 2. D.Praveena Bai attended Faculty Development Programme on“Microstrip Patch Antenna Design Techniques and Tools” organized by PVPSIT in association with NITTTR Chandigarh during 16th - 20th Sep, 2024 3. D.Praveena Bai attended Faculty Development Programme on” AI for Engineering Applications” organized by PVPSIT in association with NITTTR Chandigarh during 4th - 8th Nov, 2024. 4. Mrs. D.Praveena Bai attended FDP on INDUSTRY 4.0/5.0 conducted by PVPSIT during 15th – 24th April 2024. 5. Mrs D.Praveena Bai, attended FDP on Quality Research Paper and Funding Project Proposal Writing conducted by PVPSIT at SAGTE during 11th – 15th March 2024. 6. Mrs. D.Praveena Bai attended a oneday FDP on Practical implementation of Artificial Intelligence & IOT in Engineering Solutions Education in association with Dept. of ECE, at PVPSIT 25th JAN 2024 7. Mrs. D.Praveena Bai attended a FDP on 5G and Beyond by NITTTR, Chandigarh at PVPSIT during 5 th – 9 th February 2024 8. Mrs. D.Praveena Bai participated in six day ATAL faculty development program on Novel Materials, Devices and Applications organized by All India Council For Technical Education in association with Dept. of ECE, VRSEC during 11th - 16th December’ 2023.

9. Mrs. D.Praveena Bai participated in faculty development program on National Intellectual Property Awareness Training Program organized by CGPDTM and CSIR during 1st – 31st July, 2023.
10. Mrs. D.Praveena Bai, participated in Faculty Development Programme on Recent Trends in Wireless Communication organized by NITTTR CHANDIGARH in association with Dept. of ECE, PVPSIT during 24th - 28th April' 2023.
11. Mrs. D.Praveena Bai, participated in Faculty Development Programme on Antennas and Wireless Technologies organized by PVPSIT in association with NITTTR Chandigarh during 20th - 24th February, 2023.
12. Ms. D.Praveena Bai, participated in IP Awareness/Training program under National Intellectual Property Awareness Mission conducted by PVP Siddhartha Institute of Technology, on 23rd February' 2022.
13. Ms. D.Praveena Bai participated in Training Programme on Soft Skills organized by NITTTR Chennai during 7th - 11th February' 2022.
14. Ms. D.Praveena Bai, participated in a workshop on Recent Trends in Radar Signal Processing organized by Department of ECE, Gayatri Vidyaparishad College of Engineering for Women during 3rd – 5th January' 2022.
15. Mrs. D.Praveena Bai, participated in FDP on Design and Practical Aspects of RF Devices, MEMS and Antennas for 5G Communications, Radar and Electronic Warfare organized by VR Siddhartha Engineering College, Vijayawada, India during 14th – 16th December' 2022.
16. Mrs.D.Praveena Bai, participated in a five days STTP on Enhancing Research Capabilities for Academic Career Progression organized by NITTTR Chandigarh during 5th- 9th September' 2022.
17. Mrs. D.Praveena Bai participated in a workshop on Application Specific Integrated Circuit Design (ASIC) Using Mentor Graphics Tools organized by Department of ECE, PVPSIT during 14th – 15th December' 2021.
18. Mrs.D.Praveena Bai, participated in Faculty Development Programme on Cyber Physical Systems organized by

Velagapudi Ramakrishna Siddhartha Engineering College,
Vijayawada during 17th – 21st May' 2021.

19. Mrs.D.Praveena Bai participated in AICTE sponsored Faculty Development Programme on Millimeter Wave Frequencies Phase-II organized by Gudlavalleru Engineering College during 16th – 22nd March' 2021.
20. Mrs.D.Praveena Bai participated in AICTE sponsored Short Term Training Programme (STTP) on Effective Engineering Teaching Practices organized by PVPS Institute of Technology Vijayawada during 18th - 23rd January' 2021.
21. Mrs. D.Praveena Bai, Assistant Professor participated in Short Term Training Programme through ICT Mode on Outcome Based Curriculum – Design and Implementation organized by National Institute of Technical Teachers Training and Research (NITTTR)-Kolkata during 2nd - 13th August' 2021
22. Mrs.D.Praveenabai, Assistant Professor participated in two-week online FDP on ICT Tools for Teaching, Learning process and Institute jointly organized by Electronics and ICT Academies under the "scheme of financial assistance for setting up of Electronics and ICT Academies" of the Ministry of Electronics and Information Technology(MeitY), Government of India during 10th- 21st August,2020.
23. Mrs.D. Praveena Bai, Assistant professor, participated in a 4 day online FDP on Data Science and Machine Learning using Python organized by the Department of Electronics and Computer Engineering, K L University during 20th -23th April, 2020.
24. Mrs.D.Praveenabai, Assistant Professor participated one day FDP on Teaching Pedagogies and Active Learning organized by Department of Information Technology at PVPSIT, Kanuru on 1st February, 2020
25. Mrs. D. Praveena Bai, Assistant Professor attended one week ICT based training Programme on Control and Automation by NITTTR, Kolkata at PVPSIT during 18th - 22nd November, 2019.
26. Mrs. D. Praveena Bai, Assistant Professor attended one week ICT based Faculty Development Program on Management issues of laboratory and workshop class organized by NITTTR, Kolkata at PVPSIT during 4th – 8th November, 2019.

	<p>27. Mrs. D. Praveena Bai, Assistant Professor attended one day Faculty Development Program on Quality Assurance in Education through the Quality Circles organized by IQAC, PVPSIT on 2nd November, 2019.</p> <p>28. Mrs. D. Praveena Bai, Assistant Professor attended one week Faculty Development Program on Water Pollution and Health organized by NITTTR, Kolkata through ICT Mode in Knowledge Centre, PVPSIT during 21st – 25th October, 2019.</p> <p>29. Mrs. D. Praveena Bai, Assistant Professor attended Faculty Development Program on Modern Pedagogical Techniques and Creative problem Solving organized by the IQAC, VRSEC, Vijayawada in association with Teaching Learning Centre of Indian Institute of Technology, Hyd., during 15th -17th April, 2019.</p> <p>30. Mrs. D. Praveena Bai, Assistant Professor attended Six day Faculty Development Program on Emerging Trends in VLSI Design and Technology with Hands on Training during 28th January - 2nd February, 2019.</p> <p>31. D.Praveena Bai has attended one week ICT based Faculty Development Program on VLSI Design conducted by NITTTR Chandigarh, organized by dept. of ECE during 24th – 28th September, 2018 at PVPSIT knowledge Centre</p> <p>32. D.Praveena Bai has attended a one week FDP on “CMOS Integrated Circuits for Instrumentation and IOT Applications” conducted by VRSEC in association with NIT Warangal from 22nd to 28th January, 2018.</p>
Details of Projects Applied / Awarded	---
Any Other information	--