


Profile

Name of the Faculty	Dr. Samuyelu Bommu				
Designation	Senior Assistant Professor				
Department	ECE				
Date of Birth	01/01/1976				
DOJ the Institution	19/06/2006				
Employee ID	ECE050206				
Address for communication	D. No: 95-25-1309, 2 nd Lane, Ankamma Nagar, Guntur-522004, AP	Permanent Address		D. No: 17-1-73, Christian Palem, Narasaraopet - 522601, Palanadu, AP	
Mobile No	9052851972	Email-ID	sambommu@pvpsiddhartha.ac.in		
AICTE Unique ID	1-454503233			AAPAR ID: 340953582452	
Education Qualifications	UG	PG	Ph.D	Others	
	B.Tech. (Electronics & Communication Engineering)	M.Tech. (Radar & Microwave Engineering)	Performance Evaluation of Advanced Normalized Subband Adaptive Filtering Algorithms for System Identification and Acoustic Echo Cancellation.	---	
Work experience	Teaching	Research		Industry	Others
	22 years	--		--	--
Area of Specialization	Signal Processing				
<p>Courses taught at Under Graduate/ Post Graduate Level: (full course title only)</p> <p>UG Level :</p> <ol style="list-style-type: none"> 1. Signals & Systems 2. Digital Signal Processing 3. Electromagnetic Field Theory 4. Transmission Lines and Waveguides 5. Radar Systems 4. Telecommunications 6. Satellite Communications 7. Network Theory 					

8. Linear and Digital IC Applications
9. Data Communications
10. Microwave Engineering
11. Antenna and Wave Propagation
12. Control Systems
13. TV and Satellite Communication
14. Electromagnetic Waves & Transmission Lines

PG Level:

1. Adaptive and smart Antennas
2. Antenna Array Synthesis
3. Phased Arrays
4. Time Harmonic Electromagnetic Fields
5. Microwave Networks
6. Selected Topics in Antenna
7. Microwave Networks & Measurements

Professional Memberships & ID's :

1. ISTE-LM102517
2. ISSE-LM03748
3. ATMS-3009
4. AMIEE- AMI/24/0527

Research Publications & Guidance	National Journals	International Journals	National Conferences	International Conferences	Patents	Books / Book chapters
	--	10	--	--	01	--
	Masters	05	--	--	--	--

Details of Paper Publications (In IEEE Format only Descending order)	<p>Journals:</p> <ol style="list-style-type: none"> 1. Kalpana Koppolu, B. Samuyelu, CB Rao K, "The Effect of a Dual Oxide - Dual Gate Material and a Sensitivity Analysis on the Performance of a Junctionless Tunnel FET", Silicon, Vol.16, Issue 9, ISSN: 1876-990X, pp.3905-3915, June, 2024. (SCIE Indexed) 2. B. Samuyelu et al, "Multi-objective hybrid optimization for micro strip patch antenna design", Web Intelligence, Vol.21, Issue 4, pp.451-472, November, 2023. (SCOPUS Indexed) 3. B. Samuyelu et al, "Smart City IoT System Network Level Routing Analysis and Blockchain Security Based Implementation", Journal of Electrical Engineering & Technology, Vol.18, Issue 2, pp.1351-1368, March, 2023. (SCIE Indexed)
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4. **B. Samuyelu** et al, "Target Application Based Design Approach for RF MEMS Switches using Artificial Neural Networks", Transactions on electrical and electronic materials, Vol.23, Issue 5, pp.509-521, October, 2022 (**SCOPUS Indexed**)
5. **B. Samuyelu**, "Energy- Efficient Routing Strategies in Wireless Sensor Networks: A Review", Linguistica Antverpiensia, Vol.2021, Issue 2, pp.3302-3309, May, 2021.
6. **B. Samuyelu** and P. Rajesh Kumar, "Robust Normalized Subband Adaptive Filter for Acoustic Noise and Echo Cancellation Systems", Advances in modeling and analysis B, Vol. 62, No. 2-4, pp. 61-71, December, 2019.
7. **B. Samuyelu** and P. Rajesh Kumar, "Normalised subband adaptive filtering with extended adaptiveness on degree of subband filters", International Journal of Electronics, Vol.104, No.12, pp. 2048-2061, 2017. (**SCIE Indexed**)
8. **B. Samuyelu** and P. Rajesh Kumar, "Deterministic Initialization Principle for Normalized Subband Adaptive Filtering", International Journal of signal and imaging systems Engineering, Vol.11, No.4, pp. 246-257, 2018. (**SCOPUS Indexed**)
9. **B. Samuyelu** and P. Rajesh Kumar, "Memorized error and varying error bound for extending adaptiveness for normalized subband adaptive filtering", Alexandria Engineering Journal, Vol.57, Issue 4, pp.2445-2453, 2018. (**SCOPUS Indexed**)
10. **B. Samuyelu** and P. Rajesh Kumar, "Multi-source echo cancellation system by advanced normalized subband adaptive Filtering", International Journal of Recent Scientific Research, Vol. 9, issue 2(A), pp.23766-23773, Feb, 2018.

Masters:

1. Y. Naga Lakshmi and **B. Samuyelu**, "Low Power Allocation Based Error Improvements in Wireless 5G", International Journal of Recent Technology and Engineering (IJRTE), Vol. 8, Issue 4, ISSN: 2277-3878, pp. 11049-11052, November, 2019.
2. K Sirisha, **B. Samuyelu** and P. Karunakar, "Dynamic and energy efficient resource allocation for OFDM based cognitive radio networks", International Journal of Advance Research and Latest Trends, Vol.09, issue 4, pp.2282-2285, Nov, 2017.
3. M. Srikanth Reddy, **B. Samuyelu** and P. Karunakar, "Echo Noise reduction using dynamic selection of sub band adaptive filters", IJRCCCT, Vol.4,issue 9, pp.714- 718, September, 2015.

	<p>4. K. Sushma Rani and B. Samuyelu, “Adaptive Noise Cancellers for Corrupted Electro cardiac signals”, IJRECE, Vol.2.0, issue 5, pp. 590-596, July-Aug, 2014.</p> <p>5. Sk. Mastan Vali, B. Samuyelu and J. Kiran Chandra Sekhar, “Generation of New Complementary and Sub complementary pulse Compression code sequences”, IJERT, Vol.2.0, issue 11, pp. 3933-3938, Nov, 2013.</p> <p>Conferences:</p> <p style="text-align: center;">--- NIL ---</p>
Details of Patents Published / Granted	<p>FRAMEWORK FOR OPTIMAL CONFORMAL ANTENNA DESIGNS, Application No: 202041040269, Journal No: 43/2020, Publication Date: 23/10/2020, India</p>
Details of Certification Courses / Workshops / STTP's / FDP (participated / organized)	<p>Participated:</p> <ul style="list-style-type: none"> ➤ One Week FDP on “Recent Innovations in communication systems, AI, ML and Biomedical systems” from 20.01.2025 to 24.01.2025 organized by Electronics and ICT Academy IIT Roorkee in association with KKR & KSR Institute of Technology and Sciences, Guntur. ➤ A one day FDP on “Practical implementation of Artificial Intelligence & IoT in Engineering Solutions” on 25.01.2024 organized by Dept. of ECE PVPSIT, Kanuru, Vijayawada. ➤ One Week FDP on “5G and Beyond” from 05.02.2024 to 09.02.2024 organized by NITTTR, Chandigarh at PVPSIT, Kanuru, Vijayawada. ➤ Online NEP 2020 Orientation & Sensitization Programme from 19.07.2024 to 27.07.2024 organized MMTTC, Osmania University, Hyderabad, Telangana. ➤ One Week FDP on “Microstrip Patch Antenna Design Techniques and Tools” from 16.09.2024 to 20.09.2024 organized by NITTTR, Chandigarh at PVPSIT, Kanuru, Vijayawada. ➤ One Week FDP on “AI for Engineering Applications” from 04.11.2024 to 08.11.2024 organized by NITTTR, Chandigarh at PVPSIT, Kanuru, Vijayawada. ➤ A two-day workshop on “Antenna Design for Modern Wireless Applications” from 21.05.2024 to 22.05.2024 organized by Department of ECE, PVPSIT, Kanuru, Vijayawada ➤ A One day workshop on “The Digital Leap in Learning: GENAI Tools for Academic Empowerment” on 02.03.2024 to 15.03.2024 organized by The Research & Development Cell in association with IQAC, PVPSIT, Kanuru, Vijayawada ➤ One Week online workshop on “Quality Research Paper and Funding Project Proposal Writing” from 11.03.2024 to 15.03.2024 organized by Department of EEE, PVPSIT, Kanuru, Vijayawada.

- A One Week FDP on “Antenna & Wireless Technologies” from 20.02.2023 to 24.02.2023 organized by NITTTR, Chandigarh at PVPSIT, Kanuru, Vijayawada.
- A One Week FDP on “Recent Trends in Wireless Communication” from 24.04.2023 to 28.04.2023 organized by NITTTR, Chandigarh at PVPSIT, Kanuru, Vijayawada.
- A Two Week FDP on “Neural Networks and Fuzzy Logic” from 21.08.2023 to 01.09.2023 organized by NITTTR, Chandigarh at PVPSIT, Kanuru, Vijayawada.
- A 3-day National Workshop on “Advanced Antennas for Space, Vehicular and Ground Applications (AASVEGA)” from 7th – 9th March, 2024 organized by University College of Engineering, Osmania University, Hyderabad.
- A two-day workshop on “Development of IoT System for Targeted Applications using ARM” from 21.06.2023 to 22.06.2023 organized by Department of ECE, PVPSIT, Kanuru, Vijayawada.
- A Two Week Online FDP on “Medical Image processing” from 22.08.2022 to 02.09.2022 Jointly Organized by E & ICT Academy: NIT Warangal, PDPM IITDM Jabalpur, IIT Kanpur, NIT Patna, MNIT Jaipur, IIT Roorkee.
- A three day FDP on “Design and Practical Aspects of RF Devices, MEMS And Antennas For 5G Communications, Radar and Electronic Warfare” from 14.12.2022 to 16.12.2022 organized by Antenna Research Group, ECE Department, VRSEC, Vijayawada.
- A 3-day National Workshop on “Recent Trends in Radar Signal Processing” from 3rd – 5th January, 2022 organized by the Dept. of ECE, Gayatri Vidya Parishad College Of Engineering For Women, Visakhapatnam.
- One Week online ATAL FDP on “Overview of Speech Processing” from 13.12.2021 to 17.12.2021 organized by IIT Dharwad.
- One Week online ATAL FDP on “Wearable Devices & Antenna Technologies for IoT, Mobile and Telemedicine Applications” from 05.07.2021 to 09.07.2021 organized by Department of ECE, Saveetha School of Engineering.
- One Week Short term training programme through ICT Mode on “Essentials of OBE” from 26.07.2021 to 30.07.2021 organized by NITTR, Kolkata.
- A two days online FDP on “Microstrip Antennas for Advanced Wireless Communications” from 15th – 16th May, 2020 organized Department of ECE, KHIT in association with IETE, Vijayawada Division.

- One week FDP on “Design Thinking and Product Innovation” from 9th – 13th March, 2020 organized by the Department of Department of Mechanical Engineering, PVPSIT, Vijayawada.
- One week online FDP on “Trends in Communication” from 26th – 30th May, 2020 organized by Department of ECE, Sir CR Reddy Engineering College in associated with Averzs Technologies.
- A two week online FDP on “ICT Tools for Teaching, Learning Process & Institutes” from 10th - 21st August, 2020 Jointly organized by EICT Academies- NIT Patna, MNIT Jaipur, IIT Roorkee, IIITDM Jabalpur, IIT Guwahati.
- A One day FDP on “IPR & Innovation management in Engineering Science” on 08.10.2020 organized by IQAC, PVPSIT in Association with APSCHE.
- A one day FDP on “Intellectual Property Rights & Innovation” on 29.04.2019 organized Department of Business administration in association with IQAC.
- A one week Faculty Development Program on “New trends and Methodologies in Engineering Education” from 24th - 29th April, 2017 organized by the E & ICT Academy, NIT, Warangal at PVP Siddhartha Institute of Technology, Vijayawada
- A one-day workshop on “Innovative Teaching Strategies in Engineering Education” on 24.03.2017 organized by the Department of Mathematics, VRSEC, Kanuru, Vijayawada.
- A two-day workshop on “Advances in Mathematical Models for Image Processing” from 04.03.2016 to 05.03.2016 organized by Department of Electronics & Communications Engineering, PVPSIT, Kanuru, Vijayawada.
- A one-week AICTE recognized short term course on “Introduction to Embedded System through ICT” from 08.02.2016 to 12.02.2016 conducted by the Department of Electronics & Communications Engineering, NIT, Chandigarh at PVPSIT, Kanuru, Vijayawada.
- A one-week Faculty Development Program on “Advanced Digital Signal Processing & Applications (ADSP-A)” from 7-13 September, 2015 conducted by the Department of Electronics & Communications Engineering, VRSEC, Kanuru, Vijayawada.
- A two-day workshop on “Advanced Trends in Embedded Systems, ATES-2014” on 28th February – 1st March, 2014 conducted by the Department of Electronics and Computer Engineering and EEE, PVPSIT, Kanuru, Vijayawada.
- A two-week ISTE workshop on “Signals & Systems” from 2nd - 12th January, 2014 conducted by Indian Institute of Technology, Kharagpur.

- A two-day National Workshop on “Advances in Signal Processing (NWASP-13)” from 30-31 August, 2013 sponsored under TEQIP Phase II, organized by the Department of Electronics and Communication Engineering, AU College of Engineering (A), Andhra University, Visakhapatnam.
- A one-day research seminar on “Computer Architecture & VLSI Design Concepts” on 26th Feb, 2012 conducted by the Department of Electronics & Communications Engineering, PVPSIT, Vijayawada.
- A two-day workshop on “Lab VIEW Software” on 27th & 28th Oct, 2010 organized by the Department of Electronics and Communication Engineering, PVPSIT, Vijayawada in association with M/s Cranes Software international Ltd., Bangalore.
- A one-day workshop on “Research Methodologies in Engineering” on 23rd Oct, 2010 conducted by the Department of Information Technology, PVPSIT, Vijayawada.
- A two-week Faculty Updation Training on “System Design Using FPGA” organized by C-DAC, Hyderabad in collaboration with JNTU, Hyderabad from 18.05.2009 to 29.05.2009.
- A one week refresher course on “Effective communication” from 4th - 9th May, 2009 organized by the Department of Science and Humanities, PVPSIT, Vijayawada.
- A one-day national level workshop on “Antennas & Developments” on 7th Sept, 2007 organized by the Department of Electronics and communication Engineering, VRSEC, Vijayawada.
- A one-day Seminar on “VLSI Design” on 09.01.2007 organized by the Department of Electronics and communication Engineering, PVPSIT, Vijayawada.
- A two-day National workshop on “Wireless Communications” from 29.12.2006 to 30.12.2006 organized by the Department of Electronics and communication Engineering, VRSEC, Vijayawada.
- A two-day refresher course on “Effective Teaching Methodology” from 08.12.2006 to 09.12.2006 at VRSEC, Vijayawada.

Organized:

--- NIL ---

Details of Projects Applied / Awarded	<p>Projects Applied:</p> <ol style="list-style-type: none"> 1. Conduction of national level conferences/ Workshops/ Seminars/ brain storming sessions etc., of three days duration “Recent Advances in Machine Learning & Deep Learning” PI: Dr. B. Samuyelu CO-PI: -- Amount: Rs. 10,70,000/- Funding Agency: Interdisciplinary Cyber Physical System (ICPS) Date of submission: 13/02/2019 2. Conduction of Three Days Workshop on “Emerging and Future Technologies of Communication System Design using Software Defined Radio” PI: Dr. B. Samuyelu CO-PI: -- Amount: Rs. 1,75,000/- Funding Agency: Interdisciplinary Cyber Physical System (ICPS) Date of submission: 28/11/2019 3. A Computer Aided Assessment tool for detection and classification of abnormalities in Mammogram PI: Dr. K. Ramanjaneyulu CO-PI: Dr.B.Samuyelu, Mrs. D. Hareesha and Mrs. T. Mahalakshmi Amount: Rs. 11,14,568/- Funding Agency: Core Research Grant (SERB) Date of submission: 02/05/2022 <p>Projects Awarded:</p> <p style="text-align: center;">--- NIL ---</p>
Any Other information	---

(Dr. B. SAMUYELU)